Amendments to Claims

1. (Currently Amended) A distributed system, comprising:

master clock ~~coupled to a timing signal path, the master clock~~ having means for ~~generating~~ transferring a timing signal on ~~the~~ a timing signal path in response to a time event associated with the master clock and means for transferring a time-stamp ~~associated with the timing signal~~ via a network in response to the time event;

slave clock ~~coupled to the timing signal path, the slave clock~~ having means for receiving the timing signal via the timing signal path and means for receiving the time-stamp via the network and having means for adjusting a local time in the slave clock in response to the timing signal ~~received via the timing signal path~~ and ~~in response to~~ the time-stamp ~~received via the network~~.

2. (Original) The distributed system of claim 1, wherein the timing signal comprises at least one signal pulse which is aligned to the time event.

3. (Original) The distributed system of claim 1, wherein the means for adjusting includes means for generating a time-stamp in response to the timing signal.

4. (Previously Presented) The distributed system of claim 3, wherein the time-stamp from the master clock indicates a local time in the master clock.

5. (Cancelled) The distributed system of claim 4, wherein the means for obtaining comprises means for obtaining the time-stamp via a network.

6. (Cancelled) The distributed system of claim 4, wherein the means for obtaining comprises means for obtaining the time-stamp via the timing signal path.

7. (Original) The distributed system of claim 4, wherein the means for adjusting further comprises means for determining a correction to the local time in the slave clock in response to the time-stamps.

8. (Original) The distributed system of claim 1, wherein the timing signal comprises a continuous frequency signal.

9. (Previously Presented) A distributed system, comprising:

master clock coupled to a timing signal path, the master clock having means for generating a timing signal on the timing signal path in response to a time event associated with the master clock;

slave clock coupled to the timing signal path, the slave clock having means for adjusting a local time in the slave clock in response to the timing signal received via the timing signal path wherein the timing signal comprises a continuous frequency signal and wherein the continuous frequency signal includes a distinguished pattern which is aligned to the time event.

10. (Previously Presented) The distributed system of claim 9, wherein the means for adjusting includes means for generating a time-stamp in response to the distinguished pattern.

11. (Previously Presented) The distributed system of claim 9, wherein the means for adjusting further includes

means for obtaining a time-stamp from the master clock that indicates a local time in the master clock.

12. (Previously Presented) The distributed system of claim 11, wherein the means for obtaining comprises means for obtaining the time-stamp via a network.

13. (Previously Presented) The distributed system of claim 11, wherein the means for obtaining comprises means for obtaining the time-stamp via the timing signal path.

14. (Previously Presented) The distributed system of claim 13, wherein the time-stamp from the master clock is encoded in the continuous frequency signal.

15. (Previously Presented) The distributed system of claim 11, wherein the means for adjusting further comprises means for determining a correction to the local time in the slave clock in response to the time-stamps.

16. (Withdrawn) A clock node, comprising:

means for communication via a timing signal path;

means for generating a timing signal on the timing signal path in response to a time event.

17. (Withdrawn) The clock node of claim 16, further comprising:

means for generating a time-stamp in response to the time event;

means for transferring the time-stamp via a network.

18. (Withdrawn) The clock node of claim 16, further comprising:

means for generating a time-stamp in response to the time event;

means for transferring the time-stamp via the timing signal path.

19. (Withdrawn) The clock node of claim 16, wherein the means for generating a timing signal comprises means for generating a continuous frequency timing signal.

20. (Withdrawn) A clock node, comprising:

means for communication via a timing signal path;

means for adjusting a local time in response to a timing signal received via the timing signal path.

21. (Withdrawn) The clock node of claim 20, wherein the means for adjusting includes means for generating a time-stamp in response to the timing signal.

22. (Withdrawn) The clock node of claim 20, wherein the means for adjusting includes means for obtaining a time-stamp for the time event via a network.

23. (Withdrawn) The clock node of claim 20, wherein the means for adjusting includes means for obtaining a time-stamp for the time event via the timing signal path.

24. (Withdrawn) The clock node of claim 20, further comprising means for generating a local clock frequency by phase locking to the timing signal.